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October 26, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Title: MULTIBIT NON-VOLATILE MEMORY AND METHOD
Letters Patent No. 6,897,517
Issued: May 24, 2005
Our Reference: IMEC279.001AUS

Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP

John M. Carson
Registration No. 34,303
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Certificate

NOV 03 2006

of Correction

NOV - 6 2006

3057524
102606

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San Francisco
415-954-4114

Los Angeles
310-551-3450

Riverside
951-781-9231

San Luis Obispo
805-547-5580

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,897,517

Page 1 of 1

APPLICATION NO. : 10/603,426

ISSUE DATE : May 24, 2005

INVENTOR(S) : Van Houdt, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At Column 1, line 59, please delete, "10/56,427" and insert therefore, -- 10/156,427--.

3057499
102606

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